WEST

Freeform Search

Database: US Patents Full-Text Database; US Pre-Grant Publication Full-Text Database; JPO Abstracts Database; EPO Abstracts Database; Derwent World Patents Index; IBM Technical Disclosure Bulletins

Term:

Display: 10 **Documents in Display Format:** TI **Starting with Number** 1

Generate: ○ **Hit List** ◉ **Hit Count** ○ **Side by Side** ○ **Image**

Search | Clear | Help | Logout | Interrupt

Main Menu | Show S Numbers | Edit S Numbers | Preferences | Cases

Search History

DATE: Monday, April 21, 2003 Printable Copy Create Case

Set Name side by side	Query	Hit Count	Set Name result set
DB=USPT; PLUR=YES; OP=ADJ			
L8	L7 and (bus near8 (clock adj rate))	8	L8
L7	L6 and (memory near4 control$4)	109	L7
L6	L5 and (interface near8 (bit or byte))	119	L6
L5	l3 and l4	350	L5
L4	((memory or external) near4 bus) near8 (width or wide or bandwidth)	2346	L4
L3	((SIMM or DIMM or (memory adj module) or internal) near4 bus) near8 (width or wide or bandwidth)	796	L3
L2	(SIMM or DIMM or (memory adj module) or internal) near8 (width or wide or bandwidth)	12704	L2
L1	(SIMM or DIMM or module or internal) near8 (width or wide or bandwidth)	20314	L1

END OF SEARCH HISTORY

Set Name side by side	Query	Hit Count	Set Name result set
	DB=USPT; PLUR=YES; OP=ADJ		
L24	L10 and (point adj2 point)	1	L24
L23	L10 (point adj2 point)	0	L23
L22	l21 and strobe	1	L22
L21	L6 and (internal or external) and (register or latch$4)	2	L21
L20	L6 and strobe and (register or latch$4)	1	L20
L19	L11 and ((port or link) near8 (point adj2 point))	0	L19
L18	L11 and ((DIMM or SIMM or module) near8 (point adj2 point))	1	L18
L17	L10 and ((DIMM or SIMM or module) near8 (point adj2 point))	1	L17
L16	L14 and (port near8 (DIMM or SIMM or module) near8 (point adj2 point))	0	L16
L15	L14 and (port near8 (point adj2 point))	7	L15
L14	L13 and (port near8 (DIMM or (memory adj2 module)))	66	L14
L13	module near8 ((buffer or register) near4 port)	353	L13
L12	L11 and ((buffer or register) near4 port)	0	L12
L11	L10 or l6	6	L11
L10	(6530033 or 5815646 or 6141739 or 5825424).pn.	4	L10
L9	L6 and interface and strobe and (signal$3 adj2 line)	1	L9
L8	L6 and board and print$3 and connect$3	2	L8
L7	L6 and board	2	L7
L6	(6034878 or 6169687).pn.	2	L6
L5	(DIMM or ((dual or double) adj4 module)) near6 board near8 (connect$4 near4 bus)	3	L5
L4	(DIMM or module) near6 board near8 (connect$4 near4 bus)	183	L4
L3	(bus near4 (clock or rate)) near8 (rank or bank) near8 (factor or ratio or "1/R" or "1/N")	0	L3
L2	(bus near4 (clock or rate)) near8 (register or device) near8 (factor or ratio or "1/R" or "1/N")	15	L2
L1	(ring near4 bus) near8 (DIMM or (memory adj module)) near8 (control$4 or arbit$8)	1	L1

END OF SEARCH HISTORY